

Customer No.: 31561
Application No.: 10065,566
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In The Specification

[0015] ~~be~~ The driving circuit further includes a light-emitting device selection unit coupled to the light-emitting device driving unit for receiving a scan signal and a data signal. When the scan signal and the data signal are at logic level '1', the light-emitting device selection unit enables the light-emitting device driving unit so that the light-emitting device driving unit provides a driving current to the light-emitting device.

[0022] Fig. 2 is a timing diagram showing voltage/time relationship for the voltages V_{dd} , V_{sce} , and V_{dat} and V_{g2} in the conventional driving circuit shown in Fig. 1; and

[0034] The aforementioned discharging unit 34 315 discharges the light-emitting device 320 to the ground. In another embodiment, the discharging unit 34 315 may connect to a negative voltage terminal to increase discharge efficiency. For example, the drain terminal of the third thin film transistor (TFT3) may be connected to a voltage source Vdrv at a ground potential or a negative voltage. If the drain terminal is connected to a negative voltage, discharging rate from the light-emitting device will increase and working life of the display may increase.